

Notice of References Cited	Application/Control No. 10/062,044	Applicant(s)/Patent Under Reexamination TEIG ET AL.	
	Examiner Naum B Levin	Art Unit 2825	Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-6,131,078	10-2000	Plaisted	703/2
	B	US-6,662,323	12-2003	Ashar et al.	714/724
	C	US-			
	D	US-			
	E	US-			
	F	US-			
	G	US-			
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Pomeranz and Reddy, "On Diagnosis and Correction of Design Errors" IEEE/ACM International Conference on Computer-Aide Design, NOV. 7-11, 1993.
	V	Lai, Sastry, and Pedram, "Boolean Matching Using Binary Decision Diagrams with Applications to logic Synthesis and Verification", 1992 IEEE International Conference on Computer Design: VLSI in Computers & Processors, Oct. 11-14, 1992.
	W	Dreesen (1990) "Standard Cell Development Flow" IEEE pp. 450-455; Jan. 1990.
	X	

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.